40th SYMPOSIUM ON THE SEARCH FOR EXTRATERRESTRIAL INTELLIGENCE (SETI) – The Next Steps (A4)

SETI I : SETI Science and Technology (1)

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NEW DATA ACQUISITION AND PROCESSING SYSTEM FOR THE SETI-ITALIA DR. STELIO MONTEBUGNOLI, NATIONAL INSTITUTE FOR ASTROPHYSICS, ITALY

Abstract

The previous generation of data acquisition systems for the Seti Italia program were represented by spectrum analyzers as MSPEC, equipped with a Sharp FFT computation oriented DSP, and a Serendip IV system we had from Berkeley University at the end of the 90s. The nowadays state of the art data acquisition and processing system for the same program, are based on extremely fast Field Programmable Gate Array (FPGA) mainly developed by the CASPER group at the Berkeley University (USA). Roach-1 board are under configuration at the Medicina (Bologna) labs and this seems to be the right approach for next generation SETI extremely fast and programmable data processing, in addition such a system is modular in order to be able to fit any computation load requirement.